	And K Onder	2004	·	
	Asok K. Sarkar	2891		
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.				
1. This communication is responsive to <u>RCE filed 2/24/2005</u> .				
2. The allowed claim(s) is/are <u>1-5,10,13-16,18 and 19</u> .				
3. The drawings filed on 11/18/2003 are accepted by the Examiner.				
 4. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some* c) None of the: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No. 3. Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)). * Certified copies not received: Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application. THIS THREE-MONTH PERIOD IS NOT EXTENDABLE. 				
 5. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient. 6. CORRECTED DRAWINGS (as "replacement sheets") must be submitted. 				
(a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached				
1) hereto or 2) to Paper No./Mail Date				
(b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of				
each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).				
7. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.				
Attachment(s)	•			
1. Notice of References Cited (PTO-892)	Notice of Informal F	Patent Application (PT	O-152)	
2. Notice of Draftperson's Patent Drawing Review (PTO-948)	6. Interview Summary			
Information Disclosure Statements (PTO-1449 or PTO/SB/ Paper No./Mail Date	08), 7. Examiner's Amendi	No./Mail Date er's Amendment/Comment		
4. Examiner's Comment Regarding Requirement for Deposit	8. 🛭 Examiner's Stateme	ent of Reasons for Alle	owance	
of Biological Material	9.			
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DETAILED ACTION

Continued Examination Under 37 CFR 1.114

1. A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after final rejection. Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, the finality of the previous Office action has been withdrawn pursuant to 37 CFR 1.114. Applicant's submission filed on February 24, 2005 has been entered.

Response to Amendment

2. Applicant's explanation of the instant invention in pointing the difference with the cited prior art was found to be persuasive.

Allowable Subject Matter

- 3. Claims 1 5, 10 16, 18 and 19 are allowed.
- 4. The following is an examiner's statement of reasons for allowance:

Claims 1 – 5, 10 – 16, 18 and 19 recite, inter alia, a method of forming a semiconductor device comprising the steps of forming a charge – trapping dielectric layer over a substrate, the charge – trapping dielectric layer including a tunneling layer, a charge – trapping layer and an insulating layer, wherein the tunneling layer is disposed over the substrate, the charge – trapping layer is disposed over the tunneling layer and the insulating layer is disposed over the charge – trapping layer and forming the conductive layer into the space of the mask layer formed over the charge – trapping layer and then planarizing the conductive layer down to the horizontal surface of the

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mask. Although, Miura, US 6,469,343 teaches forming a semiconductor device comprising the steps of forming a charge – trapping dielectric layer over a substrate, the charge – trapping dielectric layer including a tunneling layer, a charge – trapping layer and an insulating layer and Boyd, US 6,461,529 teaches forming the conductor into the space of the mask layer formed over the charge – trapping layer and then planarizing the conductive layer down to the horizontal surface of the mask, neither Boyd nor Miura teaches the motivation to combine their teachings. Additionally, the art of record does not disclose or anticipate the above limitation in combination with other claim elements nor would it be obvious to modify the art of record so as to form a device including the above limitation.

Conclusion

- 5. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."
- 6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Asok K. Sarkar whose telephone number is 571 272 1970. The examiner can normally be reached on Monday Friday (8 AM- 5 PM).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, William B. Baumeister can be reached on 571 272 1722. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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7. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Asoli Ummar Sarhar Asok K. Sarkar

Asok K. Sarka April 14, 2005

Primary Examiner